

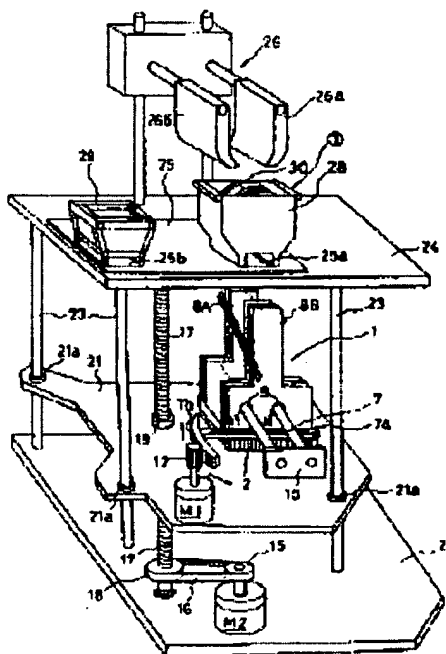
SHIFTER FOR SEMICONDUCTOR WAFER

Patent number: JP1048442
Publication date: 1989-02-22
Inventor: SHIGEMATSU NOBUAKI
Applicant: TEXAS INSTR JAPAN LTD
Classification:
- **International:** H01L21/68; B65H1/28
- **European:**
Application number: JP19870205382 19870819
Priority number(s):

Abstract of JP1048442

PURPOSE: To treat semiconductor wafers under ideal conditions, and to improve quality by providing a space changing means changing the spaces of a plurality of semiconductor wafers at the time of proceeding and the time of returning to a holding means of a semiconductor-wafer support means.

CONSTITUTION: When a plurality of semiconductor wafers are shifted between a first base section (a 'Teflon(R)' carrier) 28 and a second base section (a quartz carrier) 29, a lever 7 is turned, and the spaces of the wafers 30 are altered by a pitch changer 2 at the time of lifting and the time of lowering of a wafer supporter 1. Consequently, a large number of the wafers 30 are varied to desired spaces at a stroke and can be shifted between the 'Teflon (R)' carrier 28 and the quartz carrier 29. Accordingly, the wafers 30 can be treated at spaces proper for CVD treatment, thus improving the quality of the wafers 30, then quickly shifting the wafers.



BEST AVAILABLE COPY

Data supplied from the esp@cenet database - Patent Abstracts of Japan